

4. (Amended) The chip carrier according to claim 1 further comprising an upper surface and wherein the outer well has a first base and the inner well has a second base wherein there is a first distance between the first base and the upper surface and there is a second distance between the second base and the upper surface, the first distance greater than the second distance [extends farther away from the upper surface than the inner well].

5. (Amended) The chip carrier according to claim 1 further comprising an integrated circuit removably positioned on the base.

6. (Amended) The chip carrier according to claim 1 wherein the outer well and the inner well encircle the base [are continuous].

7. (Amended) The chip carrier according to claim 1 wherein at least one of the outer well and the inner well encircle the base [are not continuous].

21. (Newly Added) The chip carrier according to claim 1 further comprising:

an upper surface;

an integrated circuit positioned on the base and below the upper surface, the integrated circuit having leads;

wherein the inner well has a base, the leads positioned above the base and below the upper surface.

22. (Newly Added) The chip carrier according to claim 1 wherein the base, the inner well, and the outer well form an integrated circuit carrier and the chip carrier further comprises a plurality of integrated circuit carriers.

23. (Newly Added) The chip carrier according to claim 1 further comprising a cover adapted to hold an integrated circuit in the chip carrier.

24. (Newly Added) The chip carrier according to claim 1 further comprising a cover for holding an integrated circuit in the chip carrier.

25. (Newly Added) A chip carrier comprising:
a base,
an inner well formed about the periphery of the base; and

4 an outer well formed about the periphery of the inner well,

5 the chip carrier adapted to temporarily hold an integrated circuit.

1 26. (Newly Added) The chip carrier according to claim 25
2 wherein the base, the inner well, and the outer well form an integrated circuit carrier and
3 the chip carrier further comprises a plurality of integrated circuit carriers.

2 27. (Newly Added) The chip carrier according to claim 25
further comprising a cover adapted to hold an integrated circuit in the chip carrier.

1 28. (Newly Added) The chip carrier according to claim 25
2 further comprising a cover for holding an integrated circuit in the chip carrier.

BEST AVAILABLE COPY